

Abstract of the Disclosure

A communications or broadcast and communications receiver combination including microprocessor controlled synthesized communications and broadcast receivers, a keypad for entering multiple frequencies into corresponding memory addresses, 5 said frequencies defining channel(s) to which the receiver is currently tuned, frequency limits for free scanning between such limits, channels of interest to be scanned; and, importantly, two or more priority channels, said channels being assigned at least two differing priorities and microprocessor control to periodically check each priority channel and to promptly tune the receiver to any channel possessing a priority and, in turn, to 10 the channel possessing the highest priority where more than one priority channel is active at any given time.